


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN AN RF POWER AMPLIFIER																														
<div style="display: flex; justify-content: space-between;"> <div> <p>Application Number : 10/743221</p> <p>Confirmation Number: 7643</p> <p>First Named Applicant: Alan Westwick</p> <p>Attorney Docket Number: SIL.P0063</p> <p>Art Unit: 2817</p> <p>Examiner:</p> <p>Search string: (6462620 or 6549071).pn</p> </div> <div style="text-align: center;">  </div> </div>																															
<p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th>init</th> <th>Cite.No.</th> <th>Patent No.</th> <th>Date</th> <th>Patentee</th> <th>Kind</th> <th>Class</th> <th>Subclass</th> </tr> </thead> <tbody> <tr> <td></td> <td>1</td> <td>6462620</td> <td>2002-10-08</td> <td>Dupuis et al.</td> <td></td> <td>330</td> <td>264</td> </tr> <tr> <td></td> <td>2</td> <td>6549071</td> <td>2003-04-15</td> <td>Paul et al.</td> <td></td> <td>330</td> <td>252</td> </tr> </tbody> </table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6462620	2002-10-08	Dupuis et al.		330	264		2	6549071	2003-04-15	Paul et al.		330	252
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass																								
	1	6462620	2002-10-08	Dupuis et al.		330	264																								
	2	6549071	2003-04-15	Paul et al.		330	252																								
<p>Remarks</p> <p>Note: Remarks are not for responding to an office action.</p> <p>Applicants would also like to make the Examiner aware of the following commonly owned co-pending patent application: Serial No. 10/743,217, filed on 12/22/2003, attorney docket number SIL.P0064, entitled "CIRCUIT AND METHOD OF REDUCING NOISE IN AN RF POWER AMPLIFIER", by Alan L. Westwick et al. (pending).</p>																															
<p>Signature</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; text-align: center;">Examiner Name</td> <td style="width: 50%; text-align: center;">Date</td> </tr> <tr> <td style="text-align: center;">S J Notola</td> <td style="text-align: center;">1-10-06</td> </tr> </table>								Examiner Name	Date	S J Notola	1-10-06																				
Examiner Name	Date																														
S J Notola	1-10-06																														

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN AN RF POWER AMPLIFIER
-------------------------------	---

Application Number : 10/743221



Confirmation Number: 7643

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Art Unit: 2817

Examiner: Steven J. Mottola

Search string: (3684975 or 3903485 or 4024346 or 4705967 or 5220209 or 5723994 or 5936485 or 5945878 or 6259325 or 6448847 or 6809581 or 20050134378 or 20050134386 or 20050134375).pn

US Patent Documents


Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>[initials]</i>	1	3684975	1972-08-01	Valfre, Cesare		330	207P
<i>[initials]</i>	2	3903485	1975-09-01	Dolby, Ray Milton		333	14
<i>[initials]</i>	3	4024346	1977-05-01	McDermott, Noel C.		381	121
<i>[initials]</i>	4	4705967	1987-11-01	Vasile, Carmine F.		327	581
<i>[initials]</i>	5	5220209	2069-09-12	Seymour, Michael J.		326	27
<i>[initials]</i>	6	5723994	1998-03-01	Ting et al.		327	174
<i>[initials]</i>	7	5936485	1999-08-01	Rylov, Sergey V.		327	528
<i>[initials]</i>	8	5945878	1999-08-31	Westwick et al.		330	301
<i>[initials]</i>	9	6259325	2001-07-01	Ishizuka et al.		320	301
<i>[initials]</i>	10	6448847	2002-09-10	Paul et al.		327	563
<i>[initials]</i>	11	6809581	2004-10-01	Rofougaran et al.		327	563

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<i>[initials]</i>	1	20050134378	2005-06-23	Westwick et al.		330	165

	2	20050134386	2005-06-23	Westwick et al.		330	301
	3	20050134375	2005-06-23	Westwick et al.		330	98

Remarks

Note: Remarks are not for responding to an office action.

A paper IDS is being submitted on the same date as this electronic IDS.

Signature

Examiner Name	Date
S J Mottola	1-10-06